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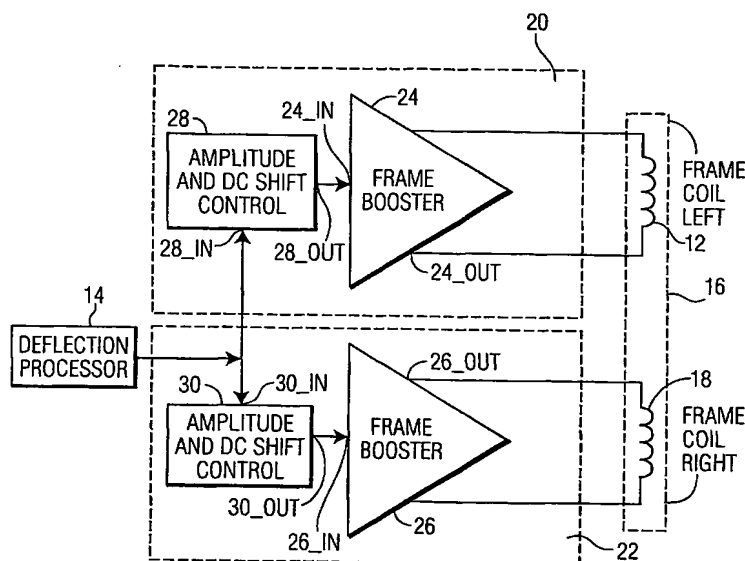
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(54) Title: DRIVE APPARATUS FOR FRAME DEFLECTION AND METHOD



(57) Abstract: A frame drive circuit for a CRT device having a deflection processor (14) which develops a deflection processor output signal includes a frame coil (16) having a first core half (12) and a second core half (18), and a first driver (20) and a second driver (22). The deflection processor output signal is applied to each of the first driver (20) and the second driver (22). Each of the first driver (20) and the second driver (22) are selectively operative independently of each other to develop respectively a first coil drive signal (24 OUT) and a second coil drive signal (26 OUT) as a function of the deflection processor output signal. The first coil drive signal (24 OUT) and the second coil drive signal (26 OUT) are applied to a respective one of the first coil half (12) and said second coil half (18).

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